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KN	AC1	US-	6,124,813	09/2000	Robertson et al.	341	143				
KN	AD1	US-	6,570,521	05/2003	Schofield, William G. J.	341	144				
KN	AE1	US-	6,577,261	06/2003	Brooks et al.	341	144				
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KN	AR	1	Adams, R. Scrambling	Adams, R. et al., "A 113-dB SNR Oversampling DAC with Segmented Noise-Shaped Scrambling," IEEE Journal of Solid-State Circuits 33:12:1879-1886 (December 1998)							
KN	AS	1	Annovazzi CMOS Ted	Annovazzi, M. et al., "A Low-Power 98-dB Multibit Audio DAC in a Standard 3.3-V 0.35-µm CMOS Technology," IEEE Journal of Solid-State Circuits 37:7:825-834 (July 2002)							
KH	AT	1	Baird, R. a pp. 13-16 (eaird, R. and Fiez, T., "Improved ΔΣ DAC Linearity Using Data Weighted Averaging," <i>IEEE</i> , p. 13-16 (1995)							
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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT

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KN	AR	<u>2</u>	Chen, F. and Leung, B., "Some Observations on Tone Behavior in Data Weighted Averaging."							
кM	AS	2	Chen, K. and Kuo, T., "An Improved Technique for Reducing Baseband Tones in Sigma-Delta Modulators Employing Data Weighted Averaging Algorithm Without Adding Dither," IEEE Transactions on Circuits and Systems- II: Analog and Digital Signal Processing 46:1:63-68 (January 1999)							
ΚŊ	AT	<u>2</u>	Fishov, A. e (2002)	t al., "Segmented	d Mismatch-Shaping D/A Conv	ersion," IEEE	=, pp. IV-679 thr	ough IV-682		
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κN	AS	. Galtor	Galton, I., "Spectral Shaping of Circuit Errors in Digital-to-Analog Converters," IEEE Transactions on Circuits and System-II: Analog and Digital Signal Processing 44:10:808-817 (October 1997)							
KN	AT	3 Grilo, Single	J. <i>et al.</i> , "A 12-mW -Chip Bluetooth Tra	ADC Delta-Sigma Modulator nsceiver," IEEE Journal of S	With 80dB of Dyololid-State Circuits	namic Range Int s 37:3:271-278 (tegrated in a March 2002)			
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KN	AT	4 Norswor	thy, S.R. et al., ed ers: Theory, Desig	ds., "Practical Measures for gn and Simulation, IEEE F	or Preventing Idle Press, pp. 185-186	Tones" in <i>Delta-S</i> (1997)	Sigma Data			
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KN	AS	<u>5</u>	Radke, R. e	Radke, R. et al., "A 14-Bit Current-Mode ΣΔ DAC Based Upon Rotated Data Weighted Averaging," IEEE Journal of Solid-State Circuits 35:8:1074-1084 (August 2000)							
KN	AT	<u>5</u>	Modulators, Transaction	/adipour, M., "Techniques for Preventing Tonal Behavior of Data Weighted Averaging Algorithm in Σ-Δ Modulators," IEEE Transactions on Circuits and Systems- II: Analog and Digital Signal Processing, Fransactions on Circuits and Systems- II: Analog and Digital Signal Processing 47:11:1137-1144 November 2000)							
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KN	AR	<u>6</u>	Converte	et al., "Simplified rs," <i>IEEE Transa</i> 14-1027 (Novem	Logic for First-Order actions on Circuits and ber 2001)	and Second d Systems-II	l-Order Misr : Analog an	match-Shaping I Id Digital Signal	Digital-to-Analog Processing	
KN	AS	<u>6</u>	Welz, J. a	Welz, J. and Galton, I., "A Necessary and Sufficient Condition for Mismatch Shaping in Multi-bit DACS," IEEE, pp. I-105 through 108 (2002)						
EN	АТ	<u>6</u>	Yasuda, A	A. et al., "A Third " IEEE Journal o	l-Order Δ-Σ Modulato of Solid-State Circuits	r Using Seco 33:12:1879	ond-Order N -1886 (Dec	loise Shaping D ember 1998)	ynamic Element	
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